## IN THE CLAIMS:

This listing of claims will replace all prior versions, and listing, of claims in the application.

### Listing of the Claims:

1. (Currently amended)  A time delay beamformer comprising a plurality of input channels, each channel having associated ~~sample~~ sampler arranged to sample an input signal carried upon the input channel at a plurality of points in time to produce a plurality of sampled signals; a processor arranged to receive said input signals and said sampled signals, or signals indicative of each of the input signals and each of the plurality of sampled signals, and arranged to generate space-time processed signals therefrom; a steering time delay arranged to introduce a steering time delay to said processed signals, or to signals indicative of said processed signals to produce at least two delayed signals; and a summer arranged to generate a beamformed output signal from the delayed signals, or from signals derived from the delayed signals.

2. (Original)   A beamformer according to claim 1 wherein the processing means is arranged to output a plurality of processed signals, and the time delay means is adapted to apply a time delay on a plurality of processed signals to form a plurality of time delayed signals, and the summation means is adapted to generate a beamformed output signal from said plurality of time delayed signals.

3. (Previously presented)  A beamformer according to claim 2 wherein there are substantially as many  processed signals produced by the processor as there are input signals.

4. (Previously presented)  A beamformer according to claim 2 wherein there are substantially as many time delayed signals as processed signals.

5. (Previously presented)  A beamformer according to claim 1 wherein the adaptive processing means is arranged to generate an input data covariance matrix from the input channels and the sampled signals.

2